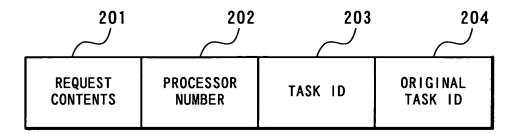
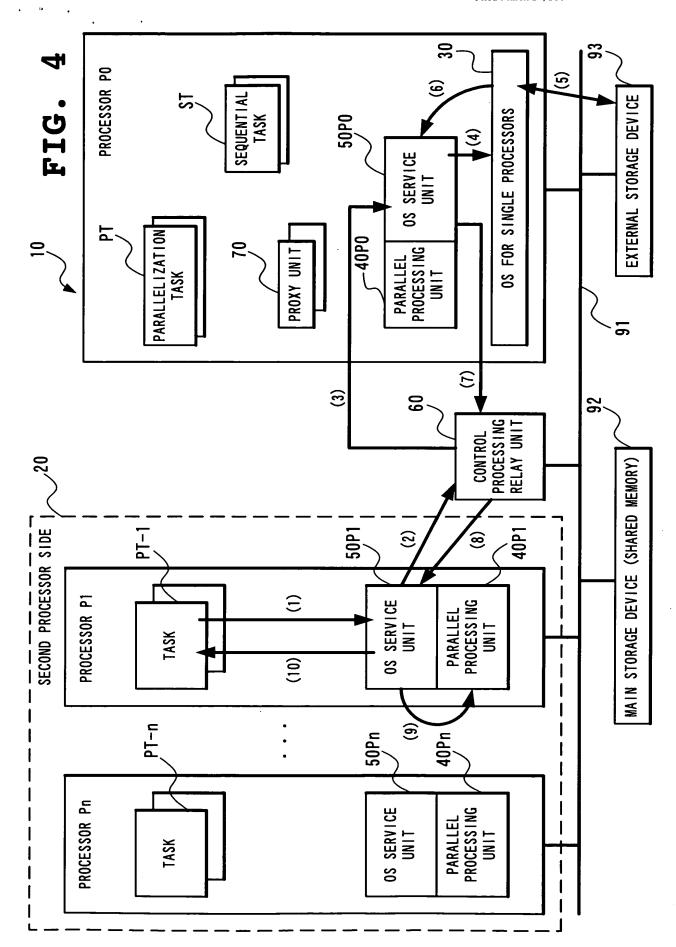
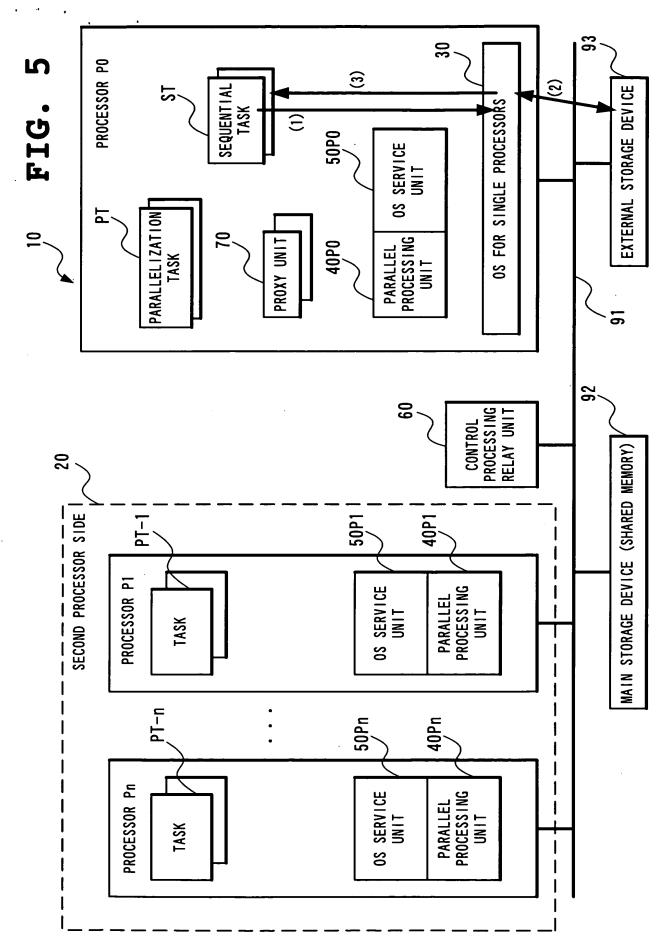
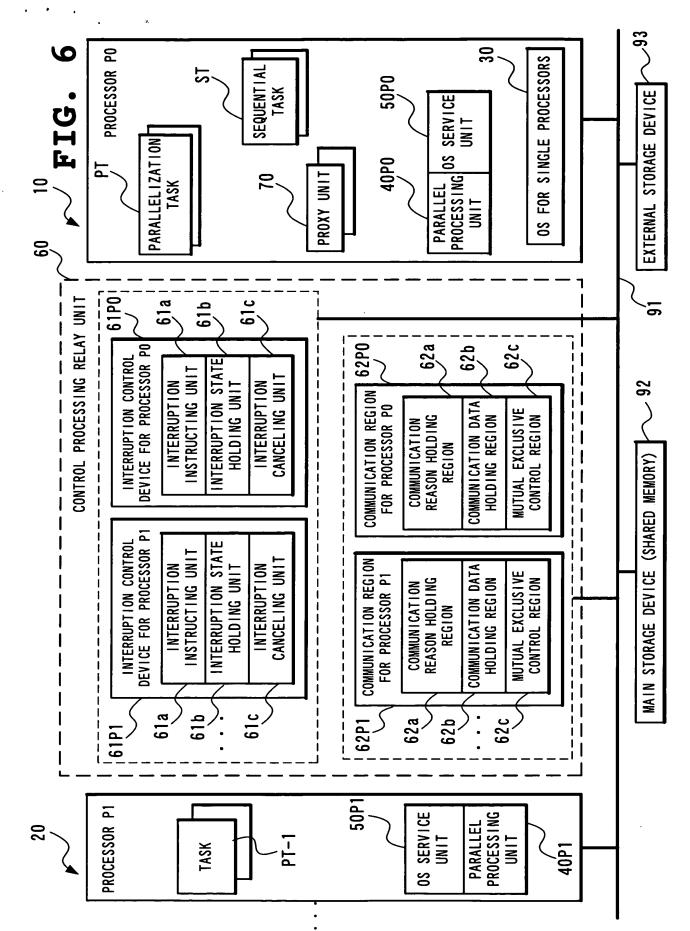


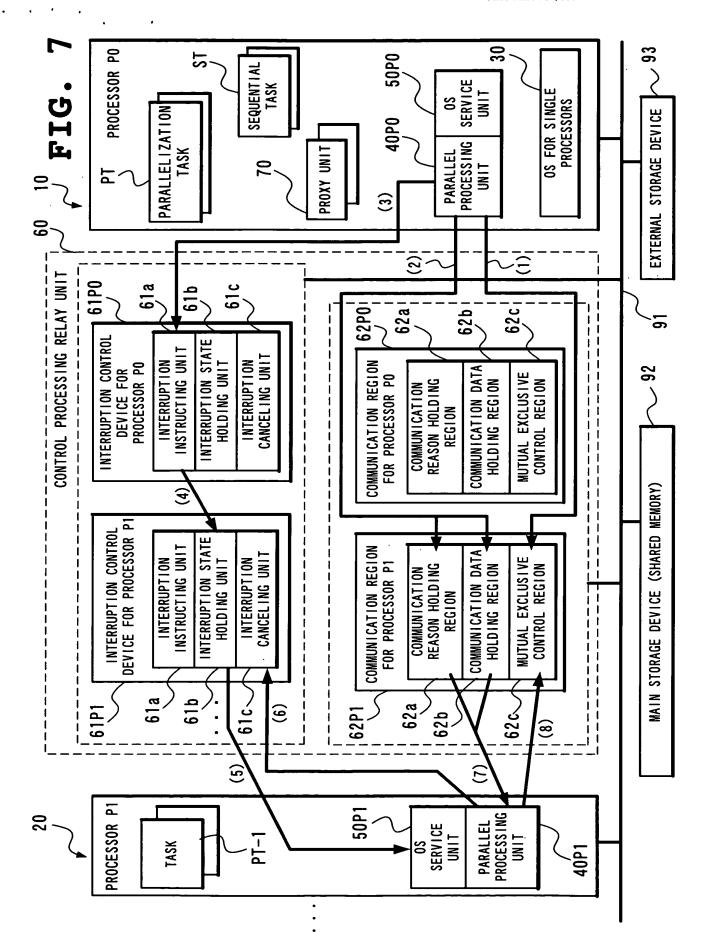
## FIG. 3

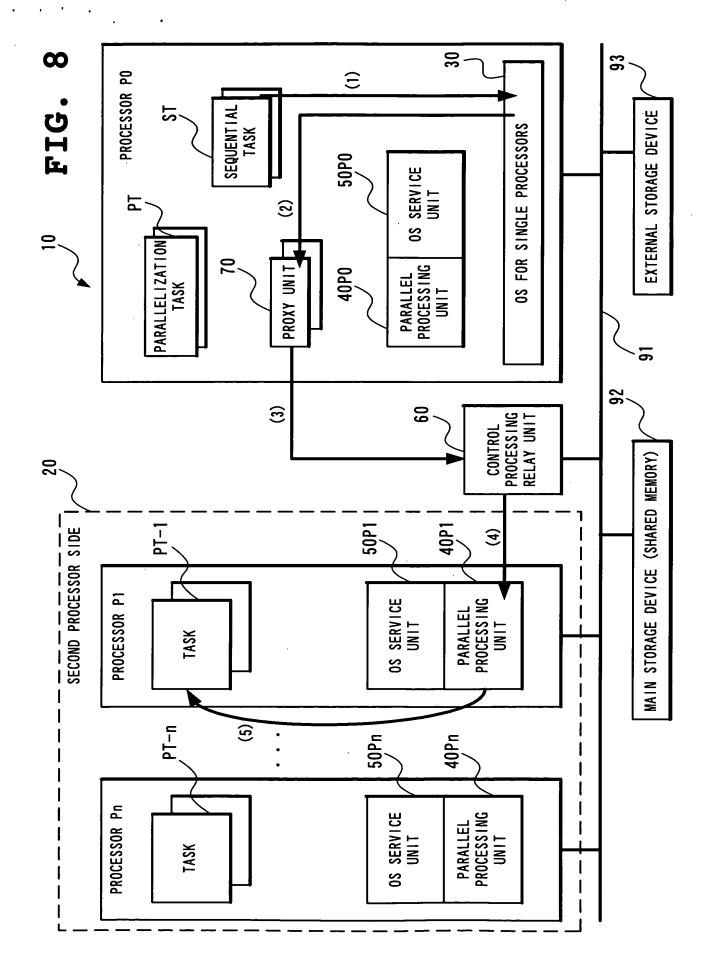


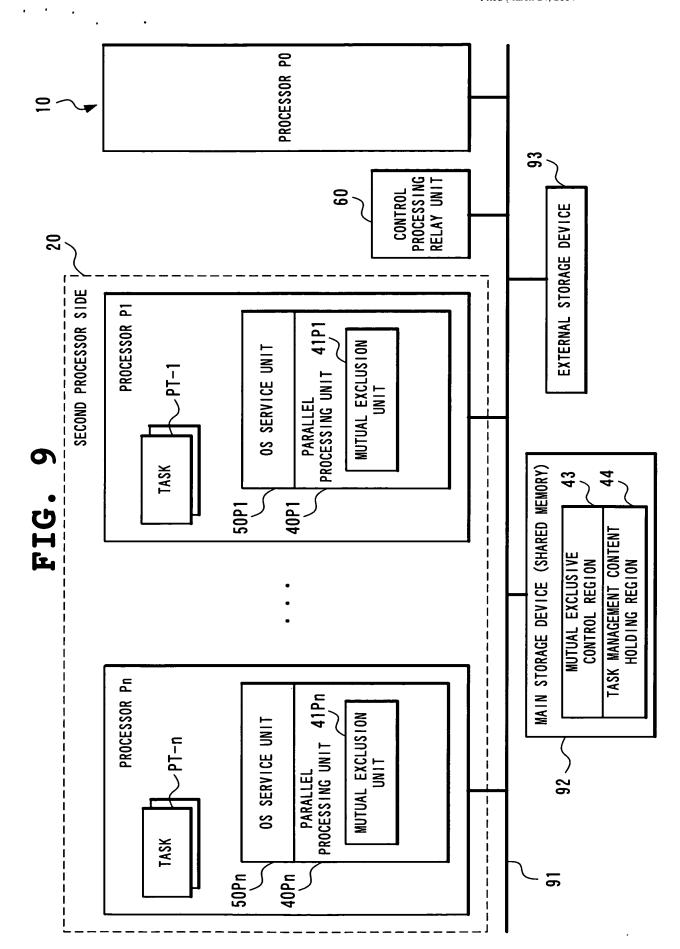


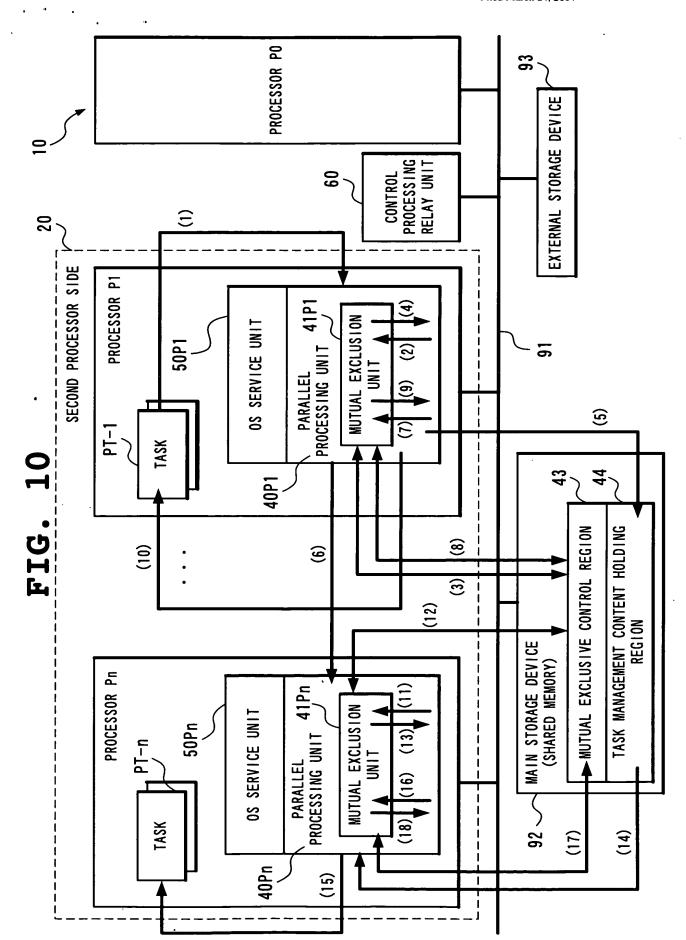


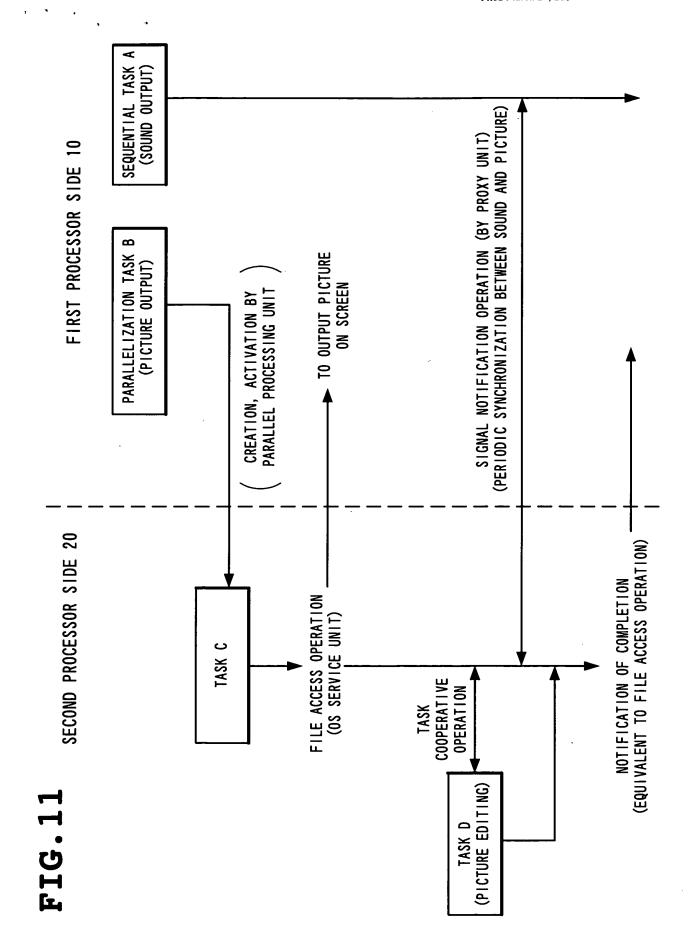


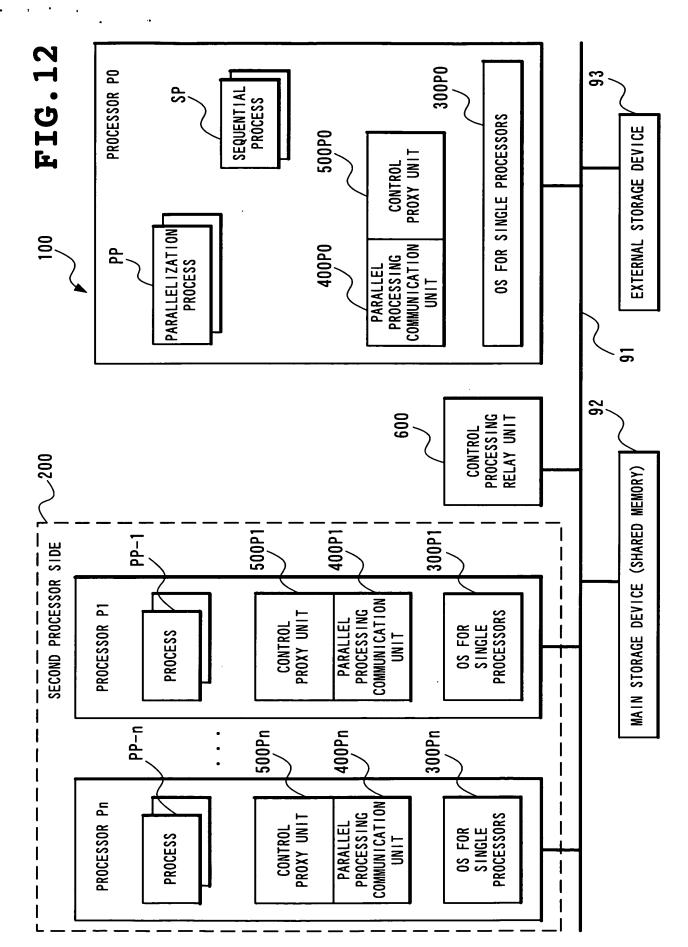


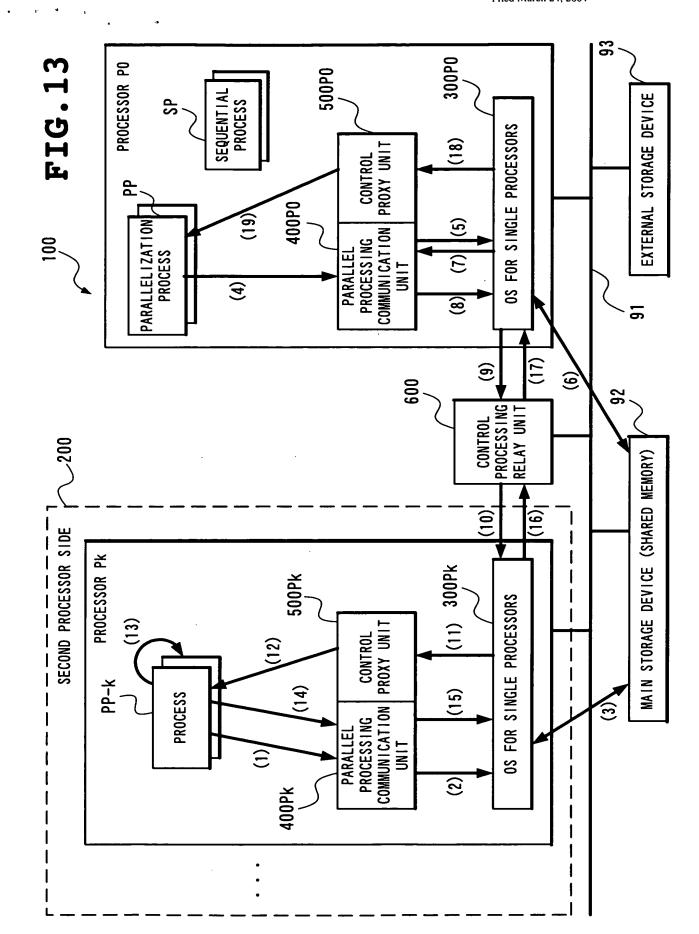


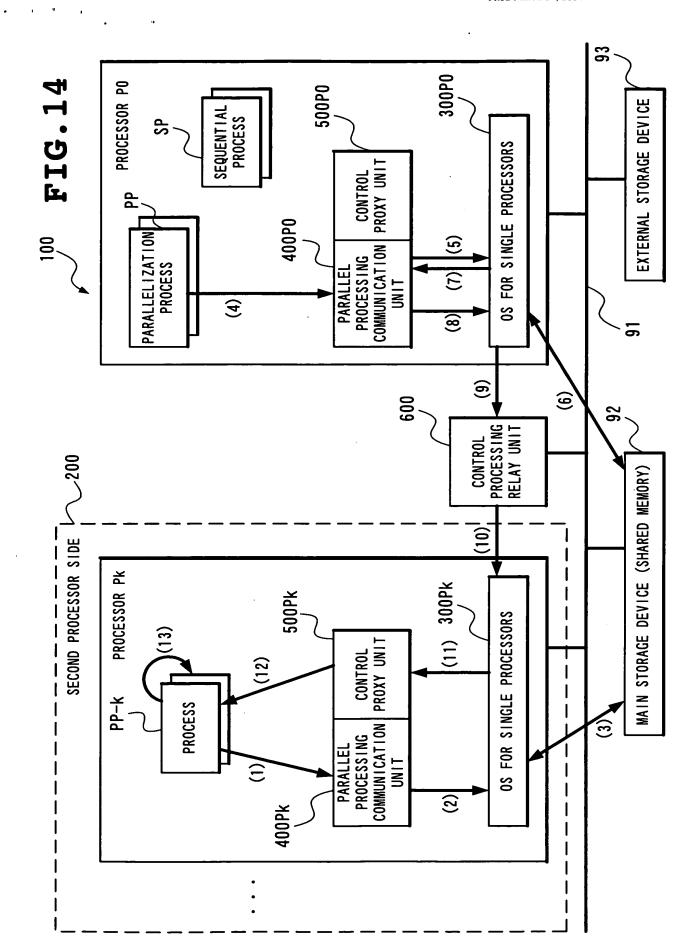


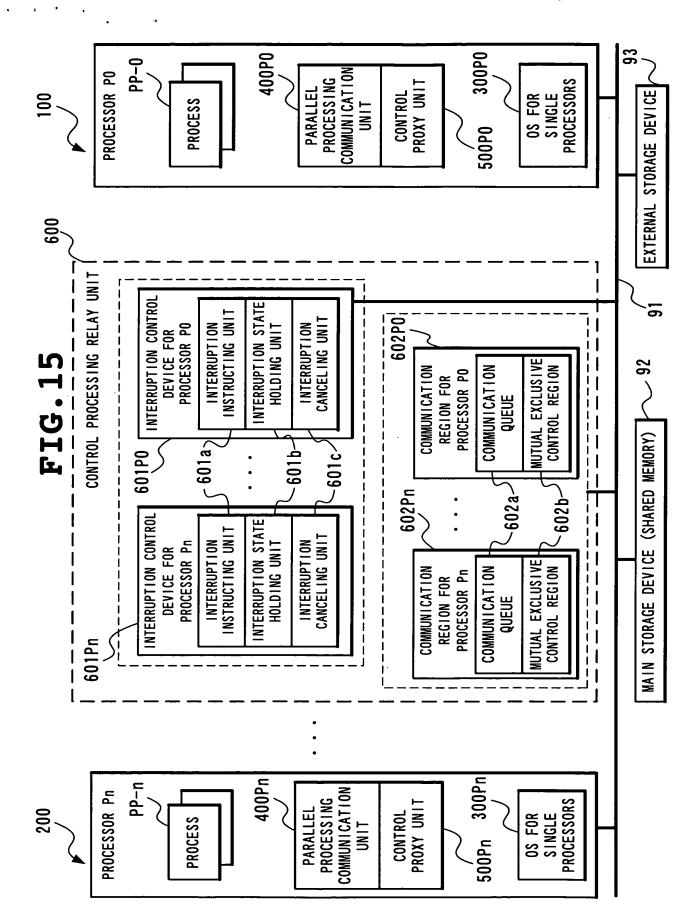


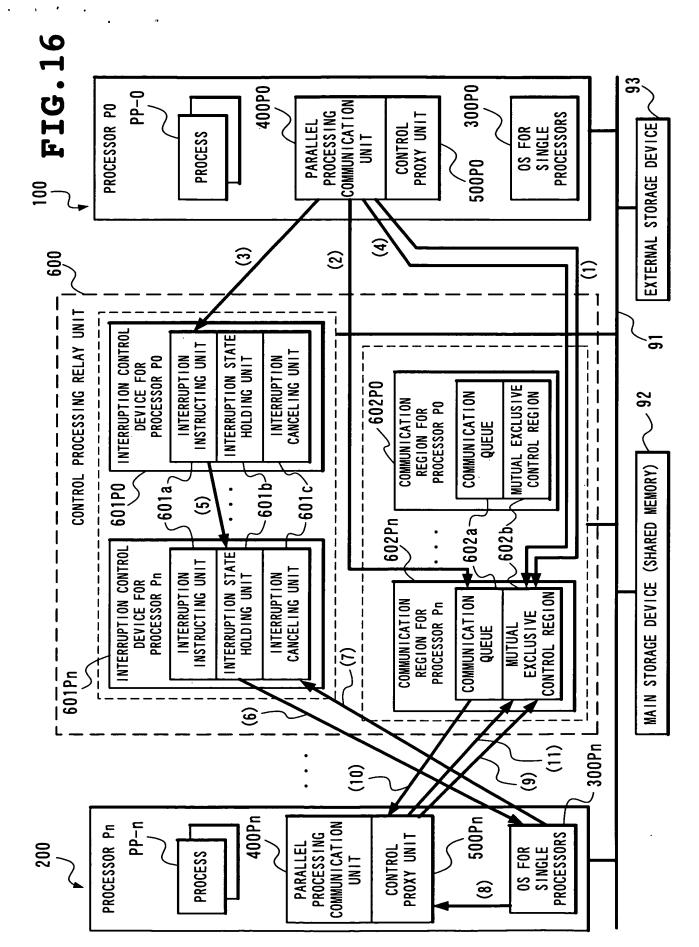


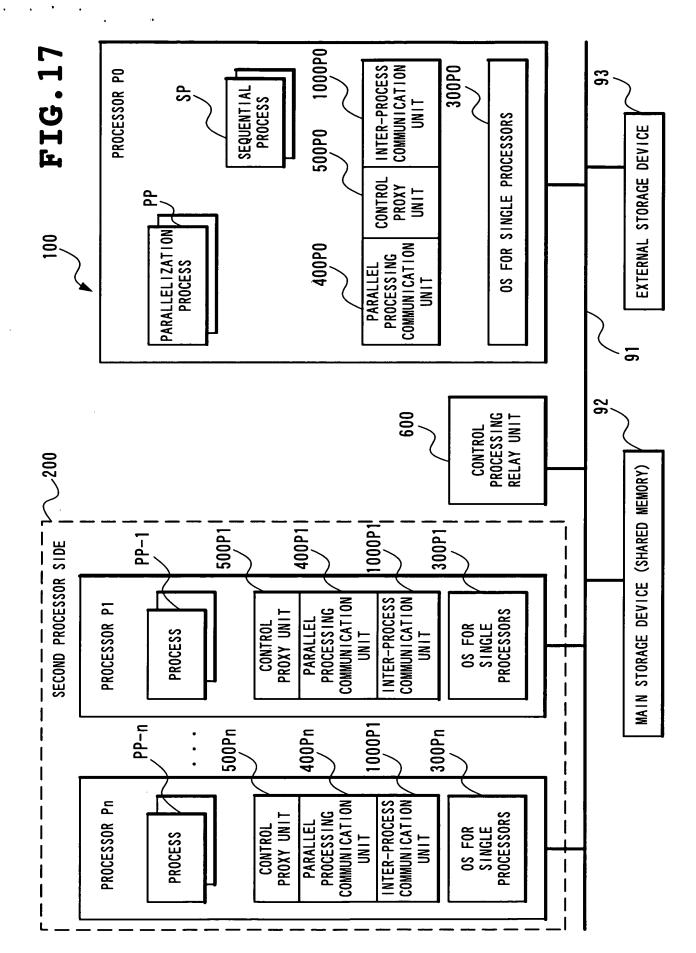


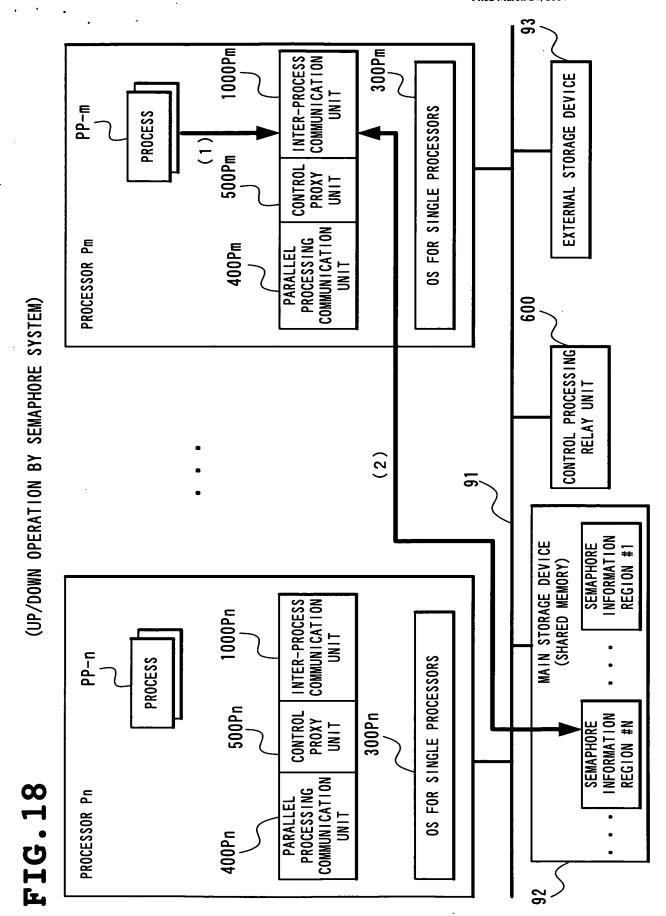


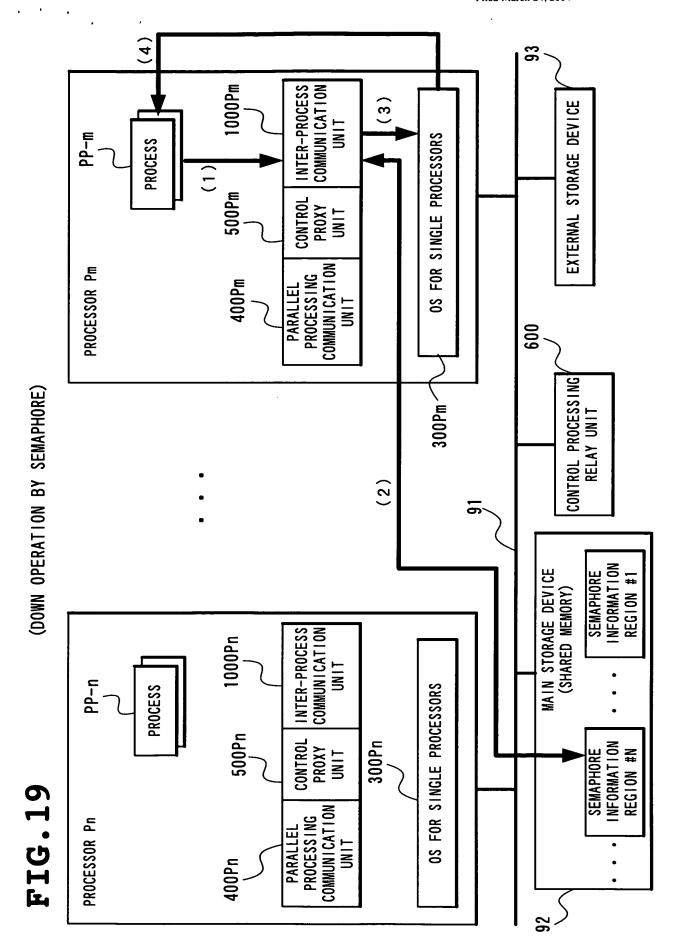


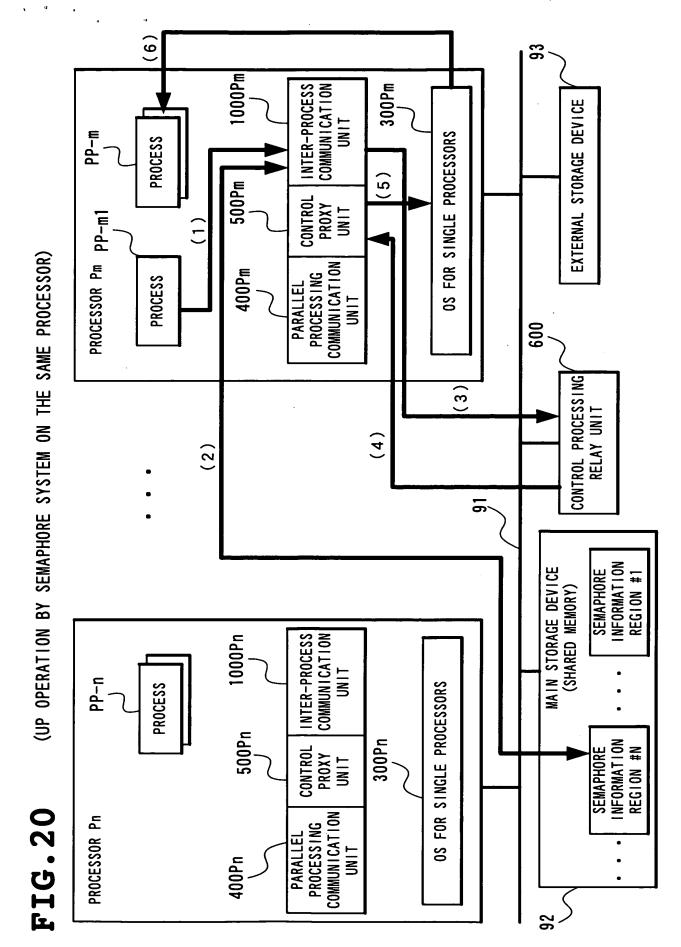












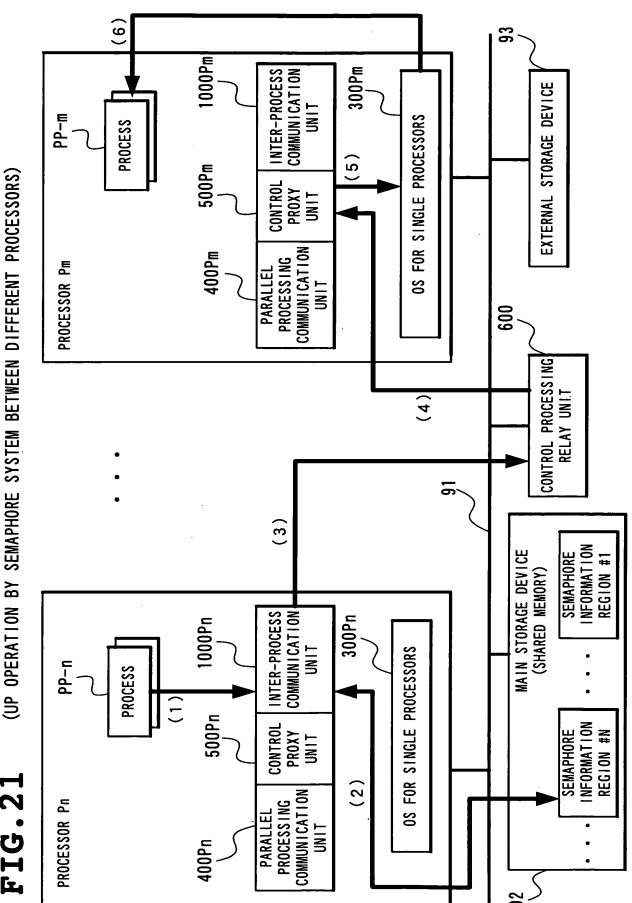
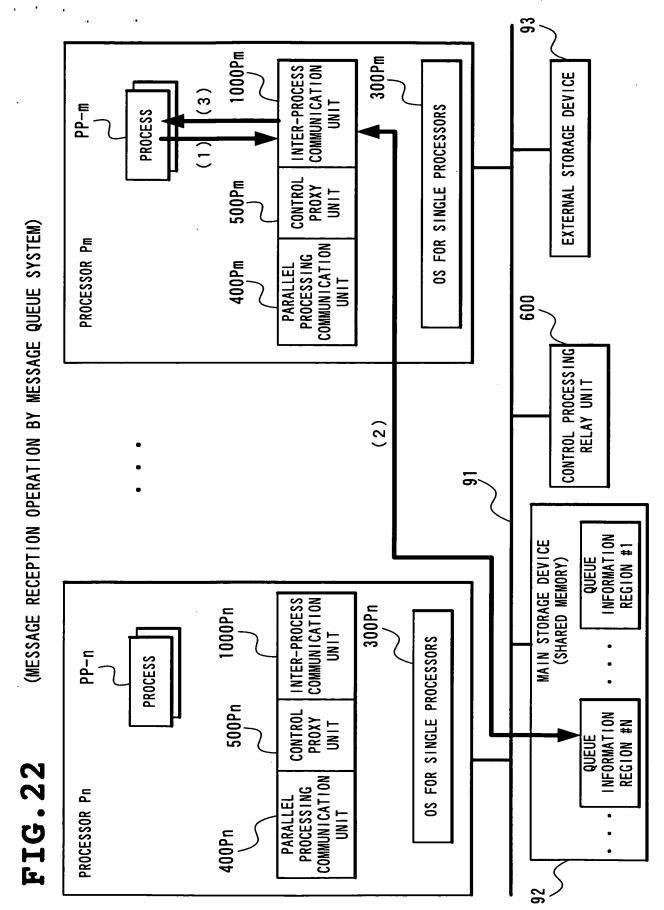
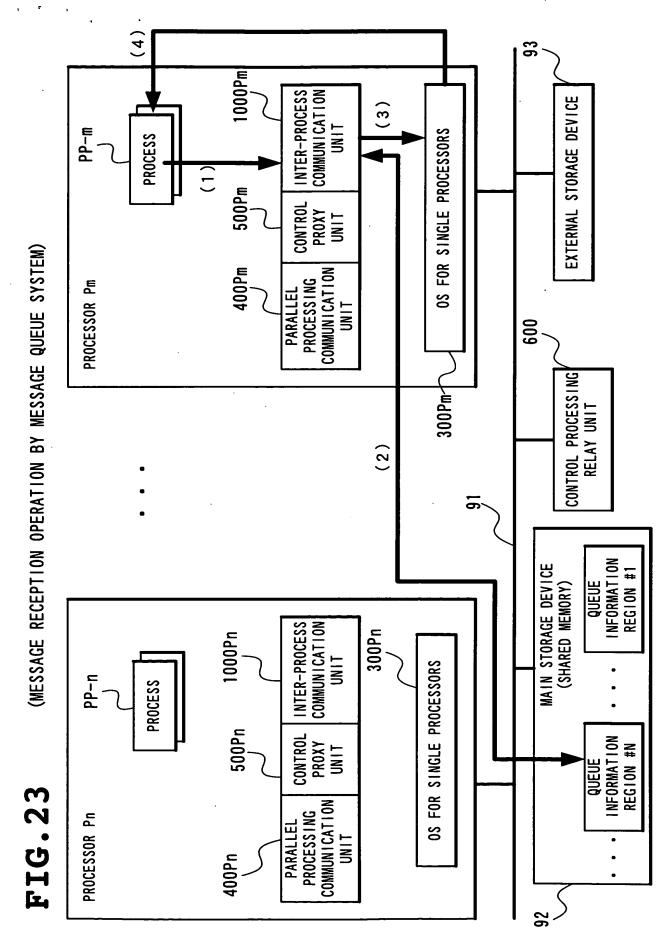
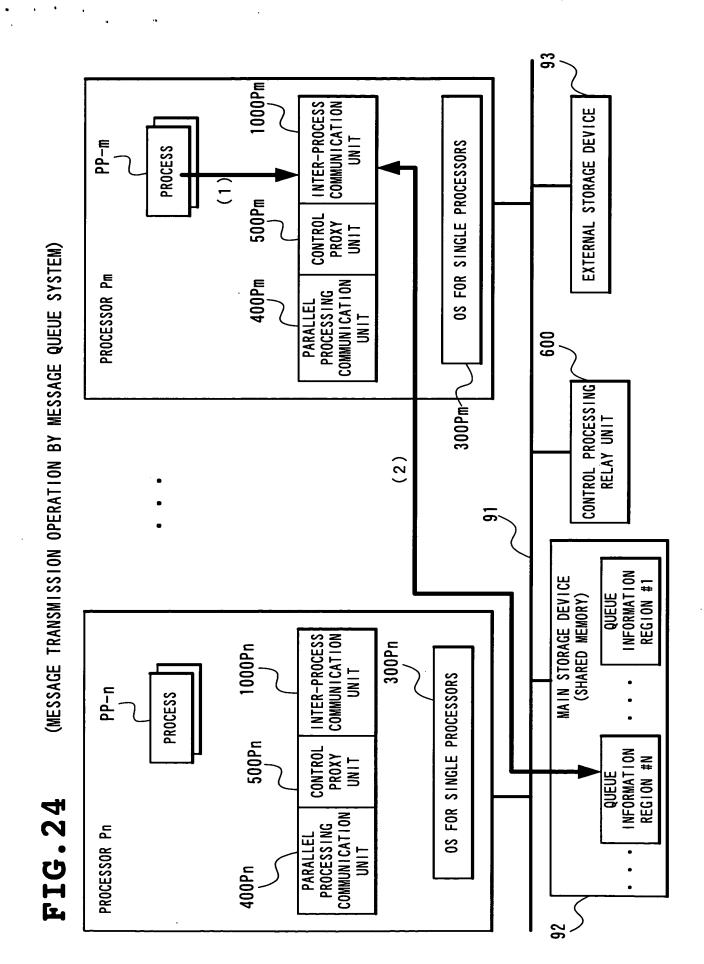
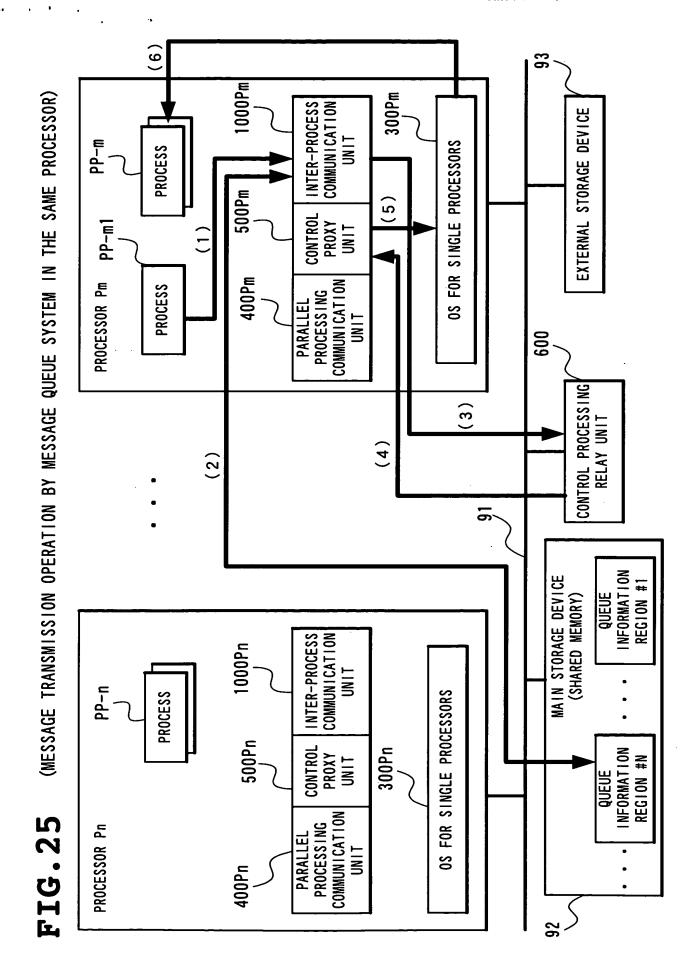


FIG. 21



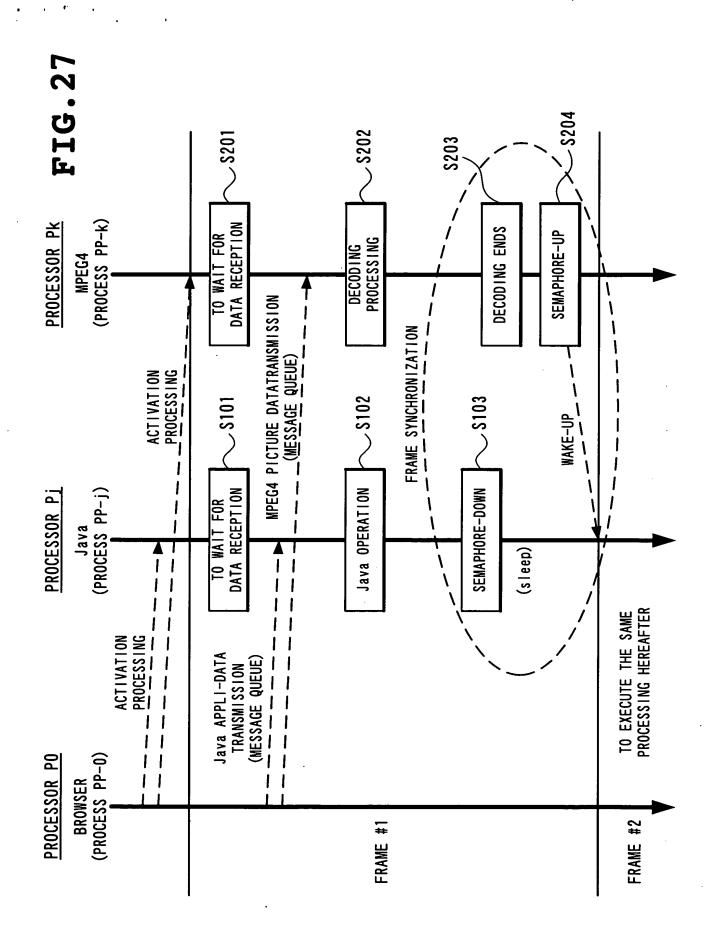






(MESSAGE TRANSMISSION OPERATION BY MESSAGE QUEUE SYSTEM BETWEEN DIFFERENT PROCESSORS) 9 300Pm INTER-PROCESS COMMUNICATION EXTERNAL STORAGE DEVICE PP-m SINGLE PROCESSORS **PROCESS** 500Pm (5)CONTROL **PROXY** FOR PROCESSOR Pm 400Pm COMMUNICATION **PROCESSING** PARALLEL S UNIT 900 CONTROL PROCESSING RELAY UNIT (4) (3) I NFORMATION MAIN STORAGE DEVICE (SHARED MEMORY) REG10N #1 QUEUE COMMUNICATION UNIT INTER-PROCESS 300Pn FOR SINGLE PROCESSORS PP-n **PROCESS**  $\overline{\mathbb{C}}$ 500Pn CONTROL PROXY UNIT **INFORMATION** REGION #N QUEUE (5)COMMUNICATION **PROCESSING** PROCESSOR Pn PARALLEL S IN

FIG. 26



**FIG.28** 

